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TRANSMITTAL		Application Number		10/618,066	
TRANSMITTAL FORM Who be used for all correspondence after initial filing)		Filing Date		July 11, 2003	
FORM		First Named Inventor		Victor Paul Schrader	
No be used for all correspondence after in	nitial filing)	Art Un	it	2817	
		Exami	ner Name	Unassigned	
Total Number of Pages in This Submission		Attorn	ey Docket Number	021343-000300US	
	ENC	LOSURE	S (Check all that apply	y)	
Fee Transmittal Form	☐ Drawin			After Allowance Communication to Group	
Fee Attached	Licensi	ng-relate	d Papers	Appeal Communication to Board of Appeals and Interferences	
☐ Amendment/Reply	Petition	1		Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)	
After Final		n to Conv onal App		Proprietary Information	
Affidavits/declaration(s)			ey, Revocation espondence Address	Status Letter	
Extension of Time Request	☐ Termin	nal Disclaimer		Other Enclosure(s) (please identify below):	
Express Abandonment Request	_ :	est for Refund		3 References	
☐ Information Disclosure Statement	∐ CD, Nu	lumber of CD(s)		Sealed envelope containing Proprietary Information Under MPEP 724.02	
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Certified Copy of Priority Document(s)	Rema	rks	The Commissioner is a Account 20-1430.	authorized to charge any additional fees to Deposit	
Response to Missing Parts/ Incomplete Application					
Response to Missing Parts under 37 CFR 1.52 or 1.53			٠		
SIGN	IATURE O	F APPL	ICANT, ATTORNEY,	OR AGENT	
Firm Townsend and To					
or Individual Chad R. Walsh			Reg. No	o. 43,235	
Signature Clad Dall					
Date April 19, 2004					
CERTIFICATE OF TRANSMISSION/MAILING					
I hereby certify that this correspondence is being hand-delivered to the USPTO to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.					
Typed or printed name					
Signature	Signature Cunette Masiello 4/20/14				
10 000 1/10 1/10					

PATENT

Attorney Docket No.: 021343-000300US

IN THE UNSTENDED ATES PATENT AND TRADEMARK OFFICE

In re application of:

Victor Paul Schrader

Application No.: 10/618,066

Filed: July 11, 2003

For: Low Voltage High Gain Amplifier

Circuits

Examiner: Unassigned

Art Unit: 2817

Confirmation No.: 4315

INFORMATION DISCLOSURE

STATEMENT UNDER 37 CFR §1.97 and

§1.98

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B and further information provided herein are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references and information provided herein be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

In accordance with 37 C.F.R. 1.56 each individual associated with the filing and - prosecution of a patent application has a duty of candor and good faith in dealing with the Office, which includes a duty to disclose to the Office all information known to that individual to be material to patentability. Pursuant to this section Applicant has included with this submission further information filed under seal for the Examiner's consideration.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no

Page 2

representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that <u>no fee is required</u> for submission of this statement. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,

Clas R. Will

Chad R. Walsh Reg. No. 43,235

TOWNSEND and TOWNSEND and CREW LLP Two Embarcadero Center, Eighth Floor San Francisco, California 94111-3834

Tel: 650-326-2400 Fax: 650-326-2422

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	1	-	of	2

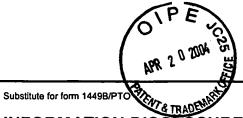
	Complete if Known	
Application Number	10/618,066	
Filing Date	July 11, 2003	
First Named Inventor	Victor Paul Schrader	
Art Unit	2817	
Examiner Name	Unassigned	
Attorney Docket Number	021343-000300US	

			U.S. PATENT DO	CUMENTS+	
		Document Number			
Examiner Initials*	Cite No.	Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS							
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	Country Code ³	Number⁴	Kind Code ⁶ (if known)	MM-DD-YYYY	Applicant of Cited Document	Passages or Relevant Figures Appear	T⁵
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Applicant's unique citation designation number (optional).
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Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3).
For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known Application Number 10/618,066 Filing Date July 11, 2003 First Named Inventor Victor Paul Schrader Art Unit 2817 Examiner Name Unassigned 021343-000300US

(use as many sheets as necessary)

2 Sheet Attorney Docket Number

	NON PATENT LITERATURE DOCUMENTS							
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T 2					
	ВС	DENNIS M. MONTICELLI, A Quad CMOS Single-Supply Op Amp with Rail-to-Rail Output Swing, IEEE Journal of Solid-State Circuits, December ,1986, pp1026-1034, Vol. SC-21, No. 6,						
	BD	G. PALMISANO, G. PALUMBO, and R. SALERNO, A 1.5-V High Drive Capability CMOS Op-Amp, IEEE Journal of Solid State Circuits, February, 1999, pp 248-252, Vol. 34, No. 2						
	BE	KA NANG LEUNG, PHILIP K. T. MOK, WING-HUNG KI, JOHNNY K. O. SIN, Three-Stage Large Capacitive Load Amplifier with Damping-Facotr-Control Frequency Compensation, IEEE Transactions on Solid-State Circuits, February, 2000, pp. 221 - 230, Vol. 35, No. 2						
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